

WHAT IS CLAIMED IS:

1. A thin film integrated circuit device comprising:

a first display area;

5 a thin film integrated circuit including a plurality of semiconductor films as a plurality of active region formed over an insulating film; and

a second display area that is formed over the first display area and connected to the thin film integrated circuit.

2. A thin film integrated circuit device comprising:

10 a first display area;

a thin film integrated circuit including a plurality of semiconductor films as a plurality of active region formed over an insulating film; and

a second display area that is formed over the first display area and connected to the thin film integrated circuit,

15 wherein display of the first display area is visible when the second display area is OFF.

3. A thin film integrated circuit device comprising:

a first display area;

20 a thin film integrated circuit including a plurality of the semiconductor films as a plurality of active region formed over one surface of an insulating film;

a metal oxide provided over another one surface of the insulating film;

and

25 a second display area that is formed over the first display area and connected to the thin film integrated circuit.

4. A thin film integrated circuit device comprising:
a first display area;
a thin film integrated circuit including a plurality of semiconductor
5 films as a plurality of active region formed over one surface of an insulating film;
a metal oxide provided over another surface of the insulating film; and
a second display area that is formed over the first display area and
connected to the thin film integrated circuit,
wherein display of the first display area is visible when the second
10 display area is OFF.

5. A thin film integrated circuit device comprising:
a first display area;
a thin film integrated circuit having a plurality of thin film transistors
15 formed over one surface of an insulating film;
a metal oxide provided over another one surface of the insulating film;
and
a second display area that is formed over the first display area and
connected to the thin film integrated circuit.

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6. A thin film integrated circuit device comprising:
a first display area;
a thin film integrated circuit having a plurality of thin film transistors
formed over one surface of an insulating film;
25 a metal oxide provided over another one surface of the insulating film;

and

a second display area that is formed over the first display area and connected to the thin film integrated circuit,

wherein display of the first display area is visible when the second display area is OFF.

7. A thin film integrated circuit device according to claim 1, wherein the first display area is a photograph.

8. A thin film integrated circuit device according to claim 2, wherein the first display area is a photograph.

9. A thin film integrated circuit device according to claim 3, wherein the first display area is a photograph.

10. A thin film integrated circuit device according to claim 4, wherein the first display area is a photograph.

11. A thin film integrated circuit device according to claim 5, wherein the first display area is a photograph.

12. A thin film integrated circuit device according to claim 6, wherein the first display area is a photograph.

13. A thin film integrated circuit device according to claim 1, wherein the

second display area is a dual emission light-emitting device.

14. A thin film integrated circuit device according to claim 2, wherein the second display area is a dual emission light-emitting device.

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15. A thin film integrated circuit device according to claim 3, wherein the second display area is a dual emission light-emitting device.

16. A thin film integrated circuit device according to claim 4, wherein the second display area is a dual emission light-emitting device.

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17. A thin film integrated circuit device according to claim 5, wherein the second display area is a dual emission light-emitting device.

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18. A thin film integrated circuit device according to claim 6, wherein the second display area is a dual emission light-emitting device.

19. A thin film integrated circuit device according to claim 3, wherein the metal film is formed from an element selected from the group consisting of W, Ti, Ta, Mo, Nd, Ni, Co, Zr, Zn, Ru, Rh, Pd, Os, and Ir, or an alloy material or a compound material which is based on the element; or an oxide of the metal compound.

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20. A thin film integrated circuit device according to claim 4, wherein the metal film is formed from an element selected from the group consisting of W, Ti, Ta, Mo, Nd, Ni, Co, Zr, Zn, Ru, Rh, Pd, Os, and Ir, or an alloy material or a compound

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material which is based on the element; or an oxide of the metal compound.

21. A thin film integrated circuit device according to claim 5, wherein the metal film is formed from an element selected from the group consisting of W, Ti, Ta,
5 Mo, Nd, Ni, Co, Zr, Zn, Ru, Rh, Pd, Os, and Ir, or an alloy material or a compound material which is based on the element; or an oxide of the metal compound.

22. A thin film integrated circuit device according to claim 6, wherein the metal film is formed from an element selected from the group consisting of W, Ti, Ta,
10 Mo, Nd, Ni, Co, Zr, Zn, Ru, Rh, Pd, Os, and Ir, or an alloy material or a compound material which is based on the element; or an oxide of the metal compound.

23. A thin film integrated circuit device according to claim 3, wherein the metal oxide is WO_2 or WO_3 .

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24. A thin film integrated circuit device according to claim 4, wherein the metal oxide is WO_2 or WO_3 .

25. A thin film integrated circuit device according to claim 5, wherein the
20 metal oxide is WO_2 or WO_3 .

26. A thin film integrated circuit device according to claim 6, wherein the metal oxide is WO_2 or WO_3 .

25 27. A thin film integrated circuit device according to claim 1, wherein the first

display area and the second display area are similar in size.

28. A thin film integrated circuit device according to claim 2, wherein the first display area and the second display area are similar in size.

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29. A thin film integrated circuit device according to claim 3, wherein the first display area and the second display area are similar in size.

30. A thin film integrated circuit device according to claim 4, wherein the first display area and the second display area are similar in size.

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31. A thin film integrated circuit device according to claim 5, wherein the first display area and the second display area are similar in size.

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32. A thin film integrated circuit device according to claim 6, wherein the first display area and the second display area are similar in size.

33. A thin film integrated circuit device according to claim 1, wherein the first display area and the second display area are combined whereby displaying letters, graphics, symbols or the combination thereof.

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34. A thin film integrated circuit device according to claim 2, wherein the first display area and the second display area are combined whereby displaying letters, graphics, symbols or the combination thereof.

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35. A thin film integrated circuit device according to claim 3, wherein the first

display area and the second display area are combined whereby displaying letters, graphics, symbols or the combination thereof.

36. A thin film integrated circuit device according to claim 4, wherein the first
5 display area and the second display area are combined whereby displaying letters, graphics, symbols or the combination thereof.

37. A thin film integrated circuit device according to claim 5, wherein the first
display area and the second display area are combined whereby displaying letters,
10 graphics, symbols or the combination thereof.

38. A thin film integrated circuit device according to claim 6, wherein the first
display area and the second display area are combined whereby displaying letters,
graphics, symbols or the combination thereof.

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39. A thin film integrated circuit device according to claim 1, wherein the first
display area displays a static image.

40. A thin film integrated circuit device according to claim 2, wherein the first
20 display area displays a static image.

41. A thin film integrated circuit device according to claim 3, wherein the first
display area displays a static image.

25 42. A thin film integrated circuit device according to claim 4, wherein the first

display area displays a static image.

43. A thin film integrated circuit device according to claim 5, wherein the first display area displays a static image.

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44. A thin film integrated circuit device according to claim 6, wherein the first display area displays a static image.

45. A thin film integrated circuit device according to claim 1, wherein the second display area displays a moving image.

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46. A thin film integrated circuit device according to claim 2, wherein the second display area displays a moving image.

47. A thin film integrated circuit device according to claim 3, wherein the second display area displays a moving image.

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48. A thin film integrated circuit device according to claim 4, wherein the second display area displays a moving image.

49. A thin film integrated circuit device according to claim 5, wherein the second display area displays a moving image.

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50. A thin film integrated circuit device according to claim 6, wherein the second display area displays a moving image.

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51. A thin film integrated circuit device according to claim 1, wherein the thin film integrated circuit device is an IC card.

52. A thin film integrated circuit device according to claim 2, wherein the thin film integrated circuit device is an IC card.

53. A thin film integrated circuit device according to claim 3, wherein the thin film integrated circuit device is an IC card.

54. A thin film integrated circuit device according to claim 4, wherein the thin film integrated circuit device is an IC card.

55. A thin film integrated circuit device according to claim 5, wherein the thin film integrated circuit device is an IC card.

56. A thin film integrated circuit device according to claim 6, wherein the thin film integrated circuit device is an IC card.